

Notice of References Cited	Application/Control No. 09/929,845	Applicant(s)/Patent Under Reexamination YUTAKA, TEIJI	
	Examiner Thomas H. Stevens	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Kutaragi et al., "1999 IEEE International Solid-State Circuits Conference" Feb. 1999 pg. 256-257
	V	Tabo et al., "Importance of CAD Tools and Methodologies in Hight Speed CPU Design" IEEE 2000 p.g 631-633.
	W	Suzuoki et al., "A Microprocessor with a 128-Bit CPU, Ten Floating-Point MAC's, Four Floating-Point Dividers, and MPEG-2 Decoder" IEEE pg. 1608-1618.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.